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[matching network to match the antenna. 3 VDD3P3 P Ampli er Power 2.5V ~ 3.6V 4 VDD3P3 P Ampli er Power 2.5V ~ 3.6V 5 VDD_RTC P NC \(1.1V\) 6 TOUT I ADC pin. It can be used to test the power-supply voltage of VDD3P3 \(Pin3 and Pin4\) and the input power voltage of TOUT \(Pin 6\). However, these two functions cannot be used simultaneously. 7 CHIP_PU I Chip Enable High: On chip works properly Low](#)

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[2. Pin De nitions 2. Pin De nitions](#)
[Figure 2-1 shows the pin layout for 32-pin QFN package. 5 Figure 2-1. Pin Layout \(Top View\)](#)
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